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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Tom E. Pearson et al.

§ Art Unit: 2833

Serial No.: 10/814,528

§ Examiner: Vanessa Mary Girardi

Filed: March 31, 2004

§ Docket: ITL.1105US
P18745For: Infrared Transmissive Integrated
Circuit Socket Cap

§ Assignee: Intel Corporation

§

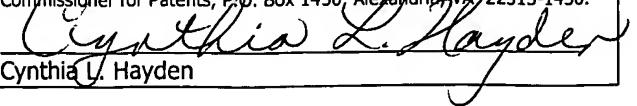
Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO PAPER NO. 40

Sir:

In response to the office action mailed October 3, 2005, please amend the above-referenced patent application as follows:

Date of Deposit: December 13, 2005
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Cynthia L. Hayden